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STATEMENT UNDER 37 CFR 3.73(b)

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pplicant/Patent Owner:	
oplication No./Patent No.: 10/789,066 Filed/Issue Date: April 18, 2006	
METHOD AND ARCHITECTURE FOR INTEGRATED CIRCUIT DESIGN AND MANUFACTURE	
JALCOMM Incorporated , a Corporation	
ame of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, e	tc.
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ne undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.	
Phillip Bennett / January 3, 2012	
Signature Date	
Attorney of Record	
Printed or Typed Name Title	

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